



Typical Feature

- ◆ Fixed Input Voltage, isolated & unregulated single output power 2W
- ◆ Continuous short circuit protection
- ◆ Operating Temperature: -40°C to +105°C
- ◆ Small SMD package, International standard pin-out
- ◆ Isolation Voltage 1500VDC
- ◆ High efficiency up to 86%
- ◆ No load input current as low as 5mA
- ◆ ESD meet Contact 8KV



Application Filed

NN2-XXSXXANT is suitable for pure digital systems, low frequency analog circuits, relay-driven circuits. It is specially designed for applications where an isolated voltage is required in a distributed power supply system. It could be widely used in the below products:

- 1. The voltage of the input power supply is relatively stable (voltage change range:±10%Vin)
- 2. Isolation between input and output is required (Isolation Voltage≤1500VDC);
- 3. Low requirements for output voltage stability and output ripple noise;

Typical Product List						
	Input Voltage	Output Voltag	e/Current	Max. Capacitive	Ripple & Noise	Efficiency
Part No	Range	Voltage	Current	Load(Max)	(Typ/Max)	(Min/Typ)
	(VDC)	(VDC)	(mA) Max / Min	u F	mVp-p	%
NN2-3V3S05ANT	3.3 (2.97-3.63)	5	400/40	2400	50/100	79/82
NN2-05S3V3ANT		3.3	400/40	2400	50/100	77/80
NN2-05S05ANT		5	400/40	2400	50/100	80/83
NN2-05S07ANT	5 (4.5-5.5)	7	285/28	1000	50/100	80/83
NN2-05S09ANT		9	222/22	1000	80/100	82/85
NN2-05S12ANT		12	167/17	560	80/100	83/86
NN2-05S15ANT		15	133/13	560	80/100	79/82
NN2-12S05ANT	12	5	400/40	2400	80/100	81/84
NN2-12S12ANT	(10.8-13.2)	12	167/17	560	80/100	83/86
NN2-12S24ANT		24	83/8	470	80/150	81/84
NN2-15S05ANT	13.5	5	400/40	2400	80/100	79/82
NN2-15S12ANT	- 16.5	12	167/17	560	80/100	81/84
NN2-15S15ANT	10.5	15	133/13	560	80/100	81/84





NN2-24S05ANT	24	5	400/40	2400	80/100	81/84
NN2-24S12ANT	(21.6-26.4)	12	167/17	560	80/100	83/86
NN2-24S24ANT		24	83/8	470	80/150	81/84

Note 1: The typical output efficiency is based on that product is full loaded and burned-in after half an hour.

Note 2: The fluctuation range of full load efficiency(%,TYP) is $\pm 2\%$, full load output efficiency= total output power/module's input power.

Note 3: Ripple & Noise Tested by twisted-pair method, for details please check Ripple & Noise Test Method.

Item	Operation	ng Condition	Min.	Тур.	Max.	Unit	
		3.3Vdc output	-	758/10	777/15		
	3.3Vdc Input	5Vdc/ 9Vdc output	-	739/20	758/25		
		12Vdc output	-	722/30	739/35		
		24Vdc output	-	758/40	777/50		
	5Vdc output	3.3Vdc output	-	500/5	513/12		
		5Vdc output	-	476/5	488/12		
		7Vdc/ 9Vdc output	-	465/10	476/20 465/30		
		12Vdc/ 15Vdc output	-	455/20			
Input Current (Full		24Vdc output	-	488/30	500/40		
load/No load)	12Vdc Input	5Vdc output	-	200/8	235/15	m <i>A</i>	
		12Vdc output	-	190/8	235/15		
		24Vdc output	-	185/10	235/15		
	15Vdc Inpu	5Vdc output	-	160/10	180/18		
		12Vdc output	-	158/10	170/18		
		15Vdc output	-	156/10	170/18		
	24Vdc input	5Vdc output	-	100/8	120/15		
		12Vdc output	-	98/8	120/15		
		24Vdc output	-	96/8	120/15		
Reflected Ripple Current		-	-	15	-		
Overshoot Voltage	3.3\	/dc Input	-0.7	-	9		
	5V	dc Input	-0.7	-	11		
	12\	/dc Input	-0.7	-	18	VD0	
	15\	/dc Input	-0.7	-	21		
	24\	/dc Input	-0.7		30		
Overshoot Current		_	-	0.8	-	А	

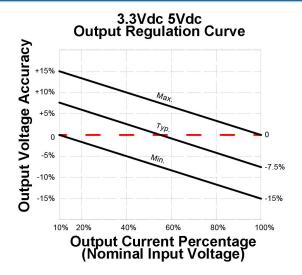


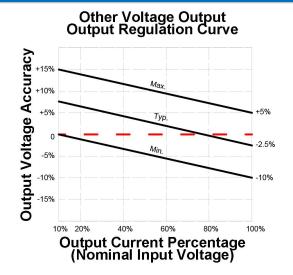


	WE	1		NN2-XXSX	XANT Se	ries		Compilant	9001	
Input Filter Type			-			Ca	pacitor Fil	Iter		
Hot Plug				Unavailable						
Output Specificatio	ns									
Item	Operatin			ng Condition		Min.	Тур.	Max.	Unit	
Output Voltage Accu	Accuracy			-		S	ee Error E	Invelope Cur	ve	
Line Regulation		Input voltage change ±1%		3.3Vdc/5V	dc output	-	-	1.5	%	
				Other voltage output		-	-	1.2	%	
Load Regulation		10%-100% load		3.3Vdc/5Vdc output		-	15	20	%	
Load Negulation	I			Other voltage output		-	10	15	/6	
Temperature Drift Coe	fficient		Fu	ıll load		-	-	±0.03	%/°C	
Short Circuit Protec	tion			-		Continuous, Self-recovery				
Seneral Specification	ons									
Item		Operating	Conditi	on	Min.	Тур).	Max.	Unit	
Insulation Withstand Voltage		Input-outpu leakage cur			1500	-		-	VDC	
Insulation Resistance	Input	Input-output, Insulation Voltage			1000	_		-	МΩ	
Isolation Capacitor		Input-output, 100KHz/0.1V			-	20		-	PF	
Operating Temperature	Tem	Temperature≥105℃,see Tem Derating Curve			-40	-		105		
Case Rising Temperature	Tes	t Environment	ature 25℃	-	15		-	°C		
Storage Temperature		-			-55	-		135		
Reflow Temperature		Peak	Value Te	mperatureTc≤2	250℃, maxi ti	ime is 60S fo	or temp ov	er 217℃		
Storage Humidity		No condensing			-	-		95	%RF	
Switching Frequency		Full load		c/5Vdc Input	-	260	260		KHz	
Switching Frequency	<u> </u>			/24Vdc Input	_	450)	-	IXI IZ	
MTBF		MIL-HDBK-	217F@2	5℃	3000				Khou	
laterial Characteri	stics									
Ca	se Materia	al		Black fla	ıme retardant	and heat re	sistant ep	oxy resin (UL	.94V-0)	
Packing Dimension		SMD Dookess		12.7X11.20X7.25 mm						
Product Weight		SMD Package			1.4g (TYP.)					
Coo	ling Meth	od				Natural air	cooling			
MC Characteristic										
ENAL		CE			CISPR32/EN55032 CLASS B (EMC Recommended Circuit)					
EMI		RE		CISPR32/EN55032 CLASS B (EMC Recommended Circuit)						
EMS		ESD IEC/E			1000-4-2 Air ±8kV, Contact ±8kV perf. Criteria B					

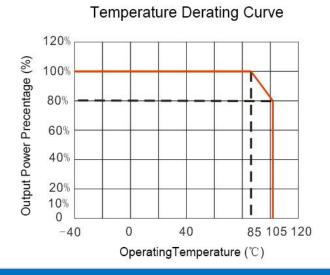


Output Voltage Error Envelope Curve





Product Character Curve

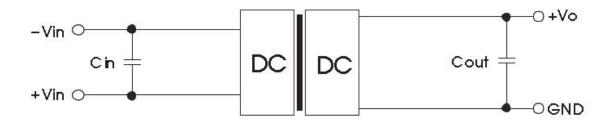


Application Circuit

1. Typical Applications

If the input and output ripples need to be further reduced, a capacitor filter network can be connected to the input and output ends. The application circuit is shown in Figure 3.

However, care should be taken to select a suitable filter capacitor. If the capacitor is too large, it is likely to cause startup problems.

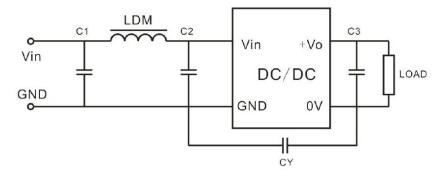


Note 1: Cin is 4.7uF/50V, Cout is 10uF/50V





2. EMC Typical Recommended Circuit



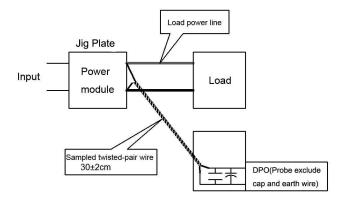
EMC Recommended Circuit

Note 2:C1,C2 is 4.7uF/50V, LDM is 6.8uH, CY is 1nF/250Vac, for C3, please refer to the Typical Circuit.

3.Ripple& Noise Test((Twisted Pair Method 20MHZ bandwidth)

- 1).12# twisted pair to connect, Oscilloscope bandwidth set as 20MHz, 100M bandwidth probe, terminated with 0.1uF polypropylene capacitor and 4.7uF high frequency low resistance electrolytic capacitor in parallel, oscilloscope set as Sample pattern.
- 2). Ripple& Noise Test Method:

Input terminal connect to power supply, output terminal connect to electronic load through jig plate. Use 30cm±2 cm sampling line. Power line selected from corresponding diameter wire with insulation according to the flow of output current.



4. Output Load Requirement

In order to ensure the converter can work reliably with high efficiency, the minimum load should not less than 10% rated load when it is used. If the needed power is indeed small, please parallel a resistor at the output side. (The actual using power and the power of the resistor should be more than 10% rated power)

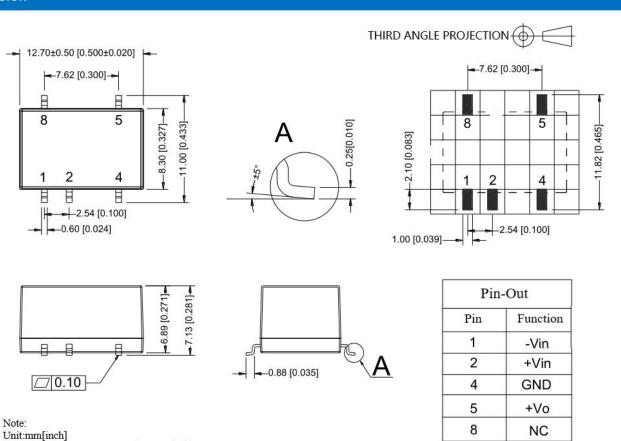


Pin section tolerance:±0.10mm[±0.004inch] General tolerance:±0.25mm[±0.010inch]

DC/DC Converter NN2-XXSXXANT Series

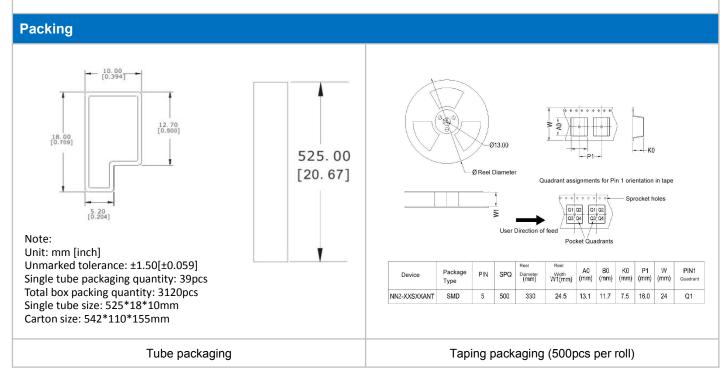


Dimension



NC pin:do not connect to any external circuit

Note: if the definition of pin is not in accordance with the model selection manual, please refer to the label on actual item.







Note:

- 1. If the product is operated under the min. required load, the product performance cannot be guaranteed to comply with all performance indexes in this datasheet;
- 2.The maximum capacitive load is tested under nominal input voltage range and full load condition;
- 3. Unless otherwise specified, data in this datasheet are tested under conditions of **Ta=25**°C, **humidity<75**% when inputting nominal voltage and outputting rated load(pure resistance load);
- 4. All index testing methods in this datasheet are based on our Company's corporate standards.
- 5. We can provide customized product service;

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